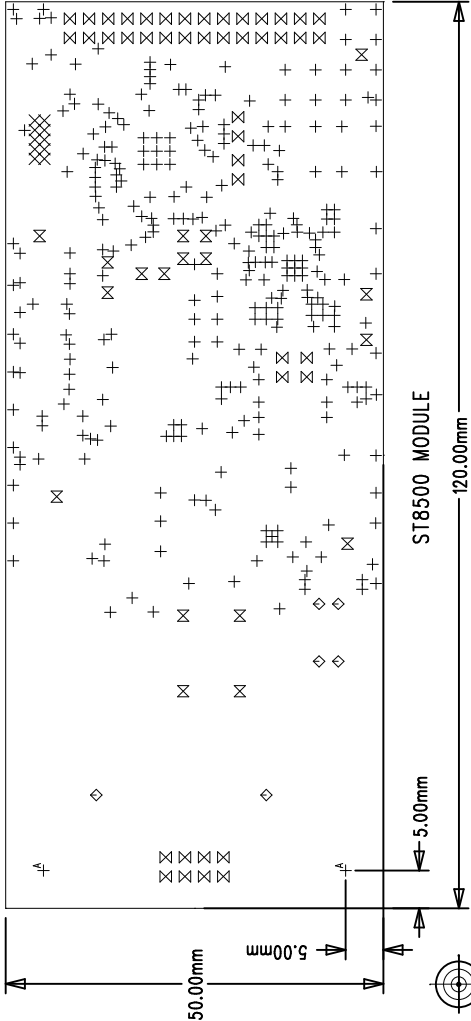


SIZE	QTY	SYM	PLATED	TOL
0.3	305	+	YES	+/-0.1
0.65	10	×	YES	+/-0.1
0.9	6	◇	YES	+/-0.1
1	44	⊗	YES	+/-0.0
1.176	18	⊗	YES	+/-0.1
3.2	2	⊕ ^A	NO	+/-0.1

PRINTED BOARD SPECIFICATIONS (Under Standard : NFC 93-713)		
Zo	LAYERS	Trace/Isolation width
Item	Minimum Value (µm)	
Trace/Isolation	150/150	
Drill/Cu	300/700	
FINITION	NiAu	Ag
	<input checked="" type="checkbox"/>	<input type="checkbox"/>
Flat Pad	NO	
Filling Via	NO	



DRILL DRAWING

Vias	LAYER STACKUP	THICKNESS	FILE NAMES
	PASTE MASK TOP	** none **	paste_top.pho
	SILSCREEN TOP	** none **	silscreen_top.pho
	SOLDER MASK TOP	** none **	solder_top.pho
	TOP	Cu 35 um	top.pho
	FR-4 400 um	FR-4 400 um	
	LAYER 2	Cu 35 um	layer2.pho
	FR-4 700 um	FR-4 700 um	
	LAYER 3	Cu 35 um	layer3.pho
	FR-4 400 um	FR-4 400 um	
	BOTTOM	Cu 35 um	bottom.pho
	SOLDER MASK BOTTOM	** none **	solder_bot.pho
	SILSCREEN BOTTOM	** none **	silscreen_bot.pho
	PASTE MASK BOTTOM	** none **	paste_bot.pho

UNLESS OTHERWISE SPECIFIED		COMPANY:		10 rue de Jouanet 35700 RENNES +33 2 23 47 04 04 WWW.ST.COM	
DIMENSIONS ARE IN INCHES AND APPLY AFTER FINISH DIMENSIONS IN BRACKETS [] ARE IN MILLIMETERS INTERPRET DRAWING PER MIL-D-8838		THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION AND IS NOT TO BE REPRODUCED OR USED FOR ANY PURPOSE OTHER THAN THAT FOR WHICH PROVIDED OR DISCLOSED IN WHOLE OR IN PART, WITHOUT THE WRITTEN CONSENT OF ST. INC.		NAME: ST8500- DEV. MODULE	
HOLE TOLERANCES PER 78027		DESIGN		SIZE	
SURFACES		RDC		APR/2017	
PART TO BE FREE OF BURRS		LAYOUT		RDC	
BEND RELIEF		DO NOT SCALE DRAWING		SCALE 1:1	
MAX		FABRICATION DRAWING		SHEET 1 OF 1	